

• General Description

It combines trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$. It combines one N channel MOSFET and one P channel MOSFET

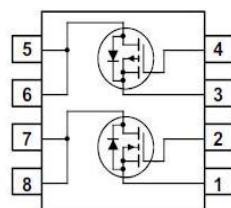
• Features

- AEC-Q101 Qualified
- Low $R_{DS(ON)}$ to minimize conductive loss
- Dual DIE in one package
- Low Thermal resistance

• Application

- BLDC Motor driver
- Load switch

• Product Summary



$$V_{DS1} = 40V$$

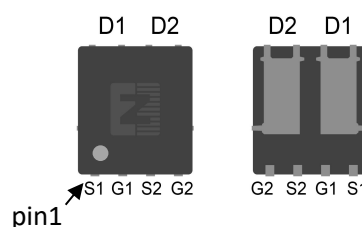
$$V_{DS2} = -40V$$

$$R_{DS(ON)1} = 8m\Omega$$

$$R_{DS(ON)2} = 12m\Omega$$

$$I_{D1} = 38A$$

$$I_{D2} = -33A$$



DFN5*6



• Ordering Information:

Part NO.	ZMCA88401N
Marking	ZMC88401
Packing Information	REEL TAPE
Basic ordering unit (pcs)	3000

• N Channel Absolute Maximum Ratings ($T_C=25^{\circ}C$)

Parameter	Symbol	Conditions	Value	Unit
Drain-Source Voltage	V_{DS}		40	V
Gate-Source Voltage ^①	V_{GS}		± 20	V
Continuous Drain Current	I_D	$T_C=25^{\circ}C$	38	A
	I_D	$T_C=75^{\circ}C$	32	A
	I_D	$T_C=100^{\circ}C$	27	A
Pulsed Drain Current	I_{DM}	Pulsed; $t_p \leq 10 \mu s$; $T_{mb} = 25^{\circ}C$;	114	A
Total Power Dissipation	P_D	$T_C=25^{\circ}C$	39	W
Total Power Dissipation	P_D	$T_A=25^{\circ}C$	3.3	W
Operating Junction Temperature	T_J		-55 to +175	$^{\circ}C$
Storage Temperature	T_{STG}		-55 to +175	$^{\circ}C$
Single Pulse Avalanche Energy	E_{AS}	$L=0.1mH$, $V_{GS}=10V$, $R_g=25\Omega$,	40	mJ
		$L=0.5mH$, $V_{GS}=10V$, $R_g=25\Omega$,	84	mJ
ESD Level (HBM)	CLASS 1C			

•P Channel Absolute Maximum Ratings ($T_C=25^{\circ}\text{C}$)

Parameter	Symbol	Conditions	Value	Unit
Drain-Source Voltage	V_{DS}		-40	V
Gate-Source Voltage ^②	V_{GS}		± 20	V
Continuous Drain Current	I_D	$T_C=25^{\circ}\text{C}$	-33	A
	I_D	$T_C=75^{\circ}\text{C}$	-27	A
	I_D	$T_C=100^{\circ}\text{C}$	-24	A
Pulsed Drain Current	I_{DM}	Pulsed; $t_p \leq 10 \mu\text{s}$; $T_{mb} = 25^{\circ}\text{C}$;	-99	A
Total Power Dissipation	P_D	$T_C=25^{\circ}\text{C}$	39	W
Total Power Dissipation	P_D	$T_A=25^{\circ}\text{C}$	3.3	W
Operating Junction Temperature	T_J		-55 to +175	$^{\circ}\text{C}$
Storage Temperature	T_{STG}		-55 to +175	$^{\circ}\text{C}$
Single Pulse Avalanche Energy	E_{AS}	$L=0.1\text{mH}$, $V_{GS}=-10\text{V}$, $R_g=25\Omega$,	60	mJ
		$L=0.5\text{mH}$, $V_{GS}=-10\text{V}$, $R_g=25\Omega$,	108	mJ
ESD Level (HBM)	CLASS 2			

•Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R_{thJC}		-	3.8	$^{\circ}\text{C/W}$
Thermal resistance, junction-ambient ^③	R_{thJA}		-	45	$^{\circ}\text{C/W}$
Soldering temperature	T_{sld}		-	260	$^{\circ}\text{C}$

•N Channel Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	40			V
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	1.3	1.7	2.5	V
Drain-Source Leakage Current	I_{DSS}	$V_{GS}=0V, V_{DS}=40V$			1.0	μA
Gate- Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$			100	nA
Static Drain-source On Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=8A$		8	12	$m\Omega$
		$V_{GS}=4.5V, I_D=6A$		13	17	$m\Omega$
Forward Transconductance	g_{FS}	$V_{DS}=5V, I_{SD}=8A$		11		s
Diode Forward Voltage	V_{FSD}	$V_{GS}=0V, I_{SD}=8A$			1.3	V

•N Channel Dynamic characteristics

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Input capacitance	C_{iss}	$f=1MHz, V_{DS}=25V$	-	1930	-	pF
Output capacitance	C_{oss}		-	154	-	
Reverse transfer capacitance	C_{rss}		-	110	-	
Gate Resistance	R_g	$f=1MHz$	-	1.4		Ω
Total gate charge	Q_g	$V_{DD}=15V, I_D=20A, V_{GS}=10V$	-	30	-	nC
	$Q_g(4.5V)$		-	16	-	
Gate - Source charge	Q_{gs}		-	5.4	-	
Gate - Drain charge	Q_{gd}		-	5.9	-	
Turn-ON Delay time	$t_{D(on)}$	$V_{GS}=10V, V_{DS}=15V, R_G=3.3\Omega, I_D=20A$	-	8	-	ns
Turn-ON Rise time	t_r		-	2.5	-	ns
Turn-Off Delay time	$t_{D(off)}$		-	41	-	ns
Turn-Off Fall time	t_f		-	8	-	ns
Reverse Recovery Time	t_{RR}	$V_{DD}=20V, dI_S/dt=100A/\mu s, I_S=20A$	-	11	-	ns
Reverse Recovery Charge	Q_{RR}		-	13	-	nC

•P Channel Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0V, I_D = -250\mu A$	-40			V
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D = -250\mu A$	-1.3	-1.7	-2.5	V
Drain-Source Leakage Current	I_{DSS}	$V_{GS} = 0V, V_{DS} = -40V$			1.0	μA
Gate- Source Leakage Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			100	nA
Static Drain-source On Resistance	$R_{DS(ON)}$	$V_{GS} = -10V, I_D = -8A$		12	17	$m\Omega$
		$V_{GS} = -4.5V, I_D = -6A$		20	24	$m\Omega$
Forward Transconductance	g_{FS}	$V_{DS} = -5V, I_{SD} = -8A$		18		s
Diode Forward Voltage	V_{FSD}	$V_{GS} = 0V, I_{SD} = -8A$			1.3	V

•P Channel Dynamic characteristics

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Input capacitance	C_{iss}	$f = 1MHz, V_{DS} = -25V$	-	3430	-	pF
Output capacitance	C_{oss}		-	262	-	
Reverse transfer capacitance	C_{rss}		-	206	-	
Gate Resistance	R_g	$f = 1MHz$	-	8.6		Ω
Total gate charge	Q_g	$V_{DD} = -15V,$ $I_D = -20A,$ $V_{GS} = -10V$	-	56	-	nC
	$Q_g(-4.5v)$		-	25	-	
Gate - Source charge	Q_{gs}		-	7.6	-	
Gate - Drain charge	Q_{gd}		-	10.8	-	
Turn-ON Delay time	$t_{D(on)}$	$V_{GS} = -10V, V_{DS} = -15V,$ $R_G = 3.3\Omega, I_D = -20A$	-	20	-	ns
Turn-ON Rise time	t_r		-	174	-	ns
Turn-Off Delay time	$t_{D(off)}$		-	43	-	ns
Turn-Off Fall time	t_f		-	10.4	-	ns
Reverse Recovery Time	t_{RR}	$V_{DD} = -20V, di_S/dt =$ $100A/\mu s, I_S = -20A$	-	58	-	ns
Reverse Recovery Charge	Q_{RR}		-	75	-	nC

• N Channel characteristics curve

Fig.1 Gate-Charge Characteristics

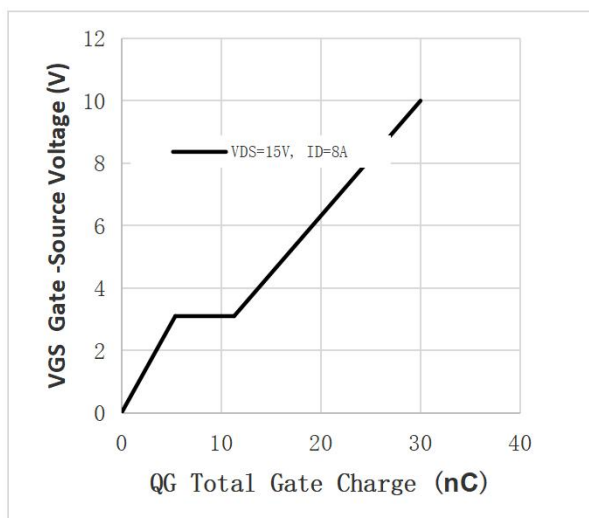


Fig.2 Capacitance Characteristics

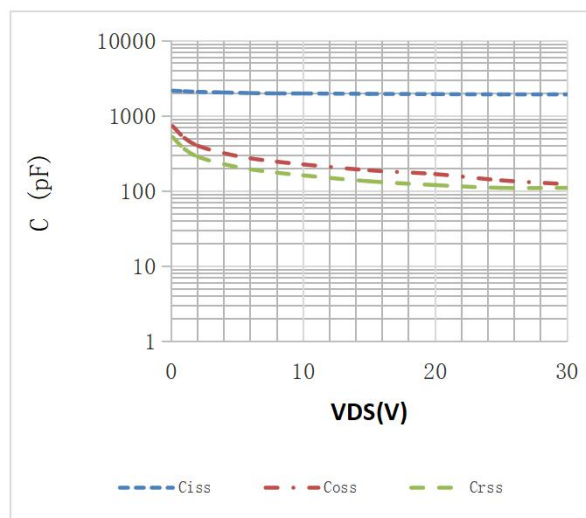


Fig.3 Power Dissipation

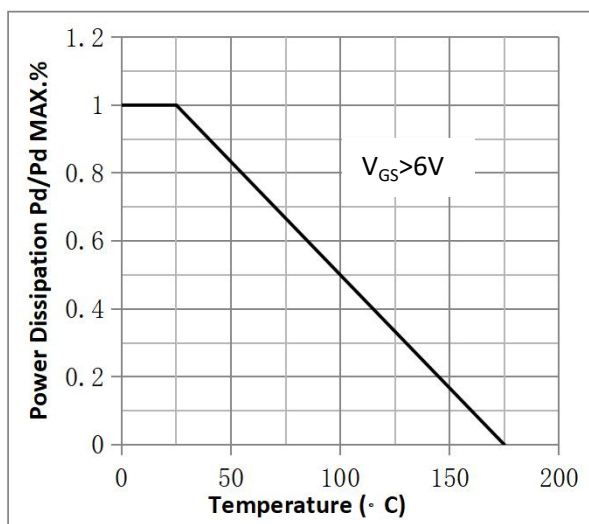


Fig.4 Typical output Characteristics

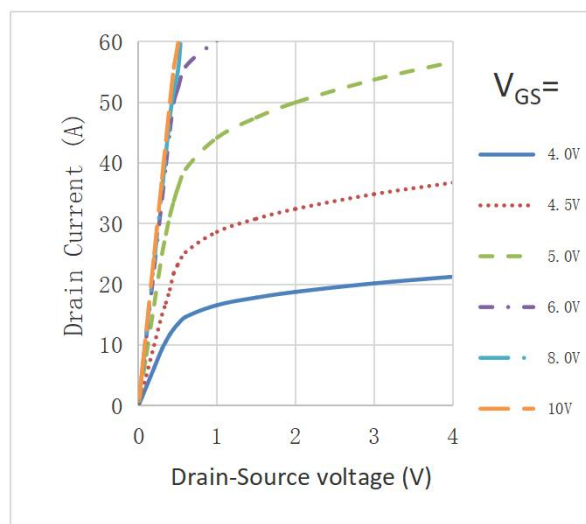


Fig.5 Threshold Voltage V.S Junction Temperature

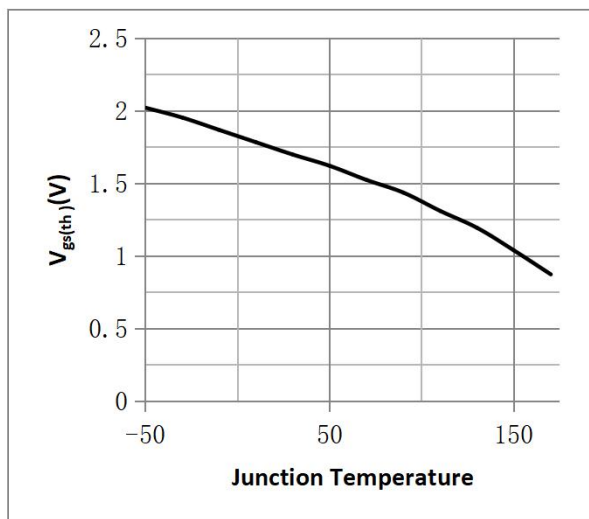


Fig.6 Resistance V.S Drain Current

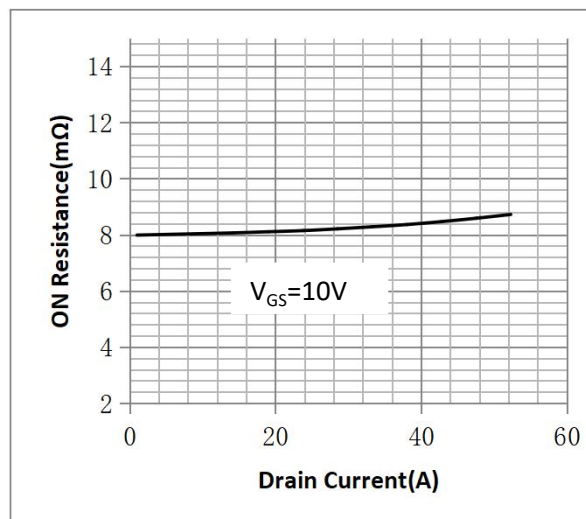


Fig.7 On-Resistance VS Gate Source Voltage

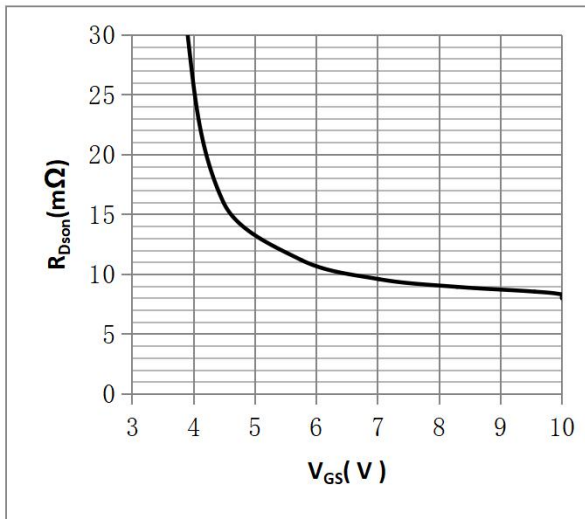


Fig.8 On-Resistance V.S Junction Temperature

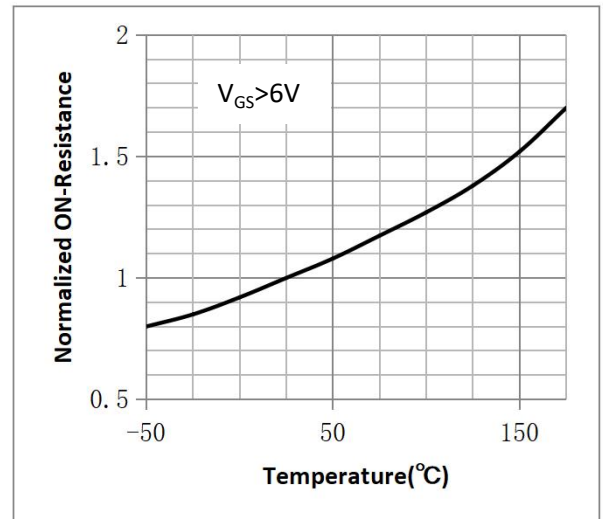


Figure 9. Diode Forward Voltage vs. Current

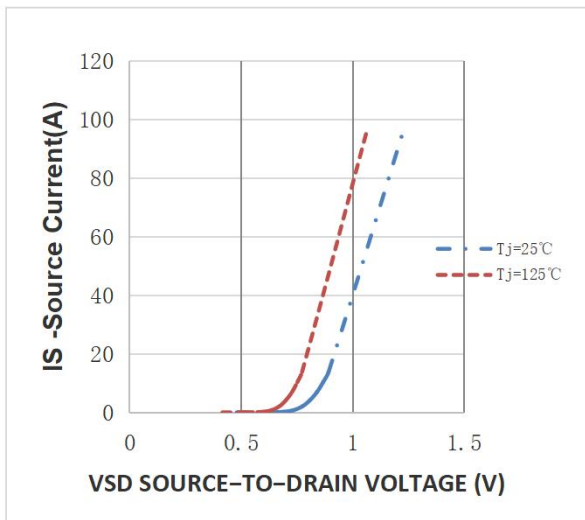


Figure 10. Transfer Characteristics

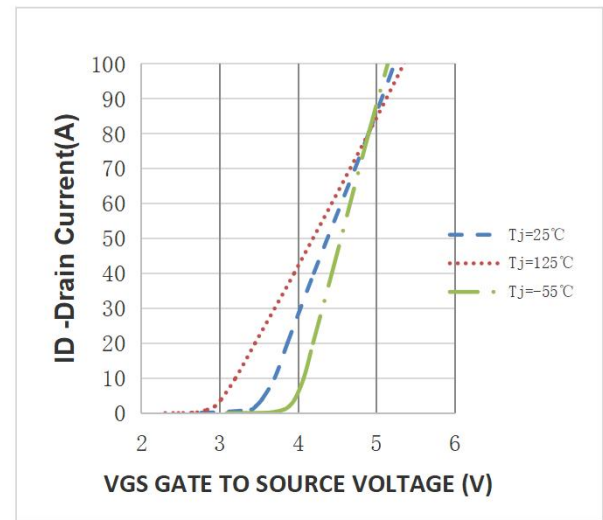


Fig.11 SOA Maximum Safe Operating Area

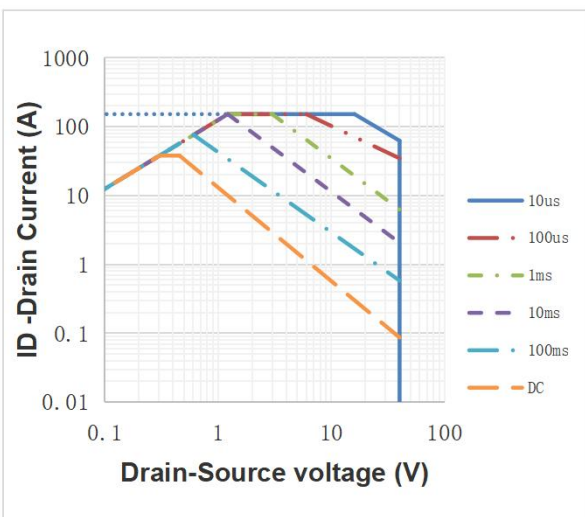
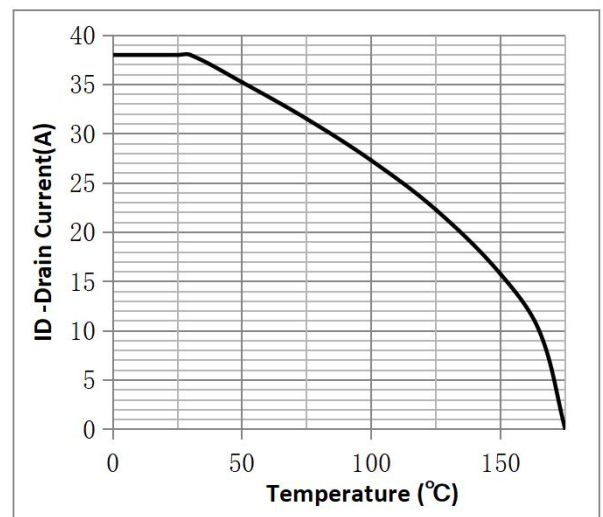


Fig.12 ID vs. Case Temperature^④



•p Channel characteristics curve

Fig.1 Gate-Charge Characteristics

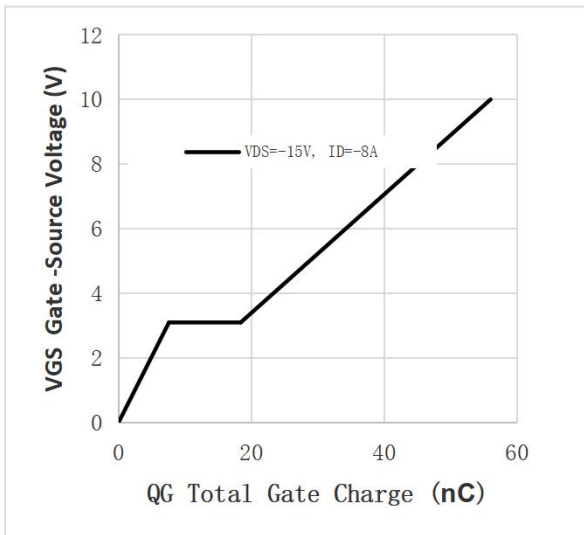


Fig.2 Capacitance Characteristics

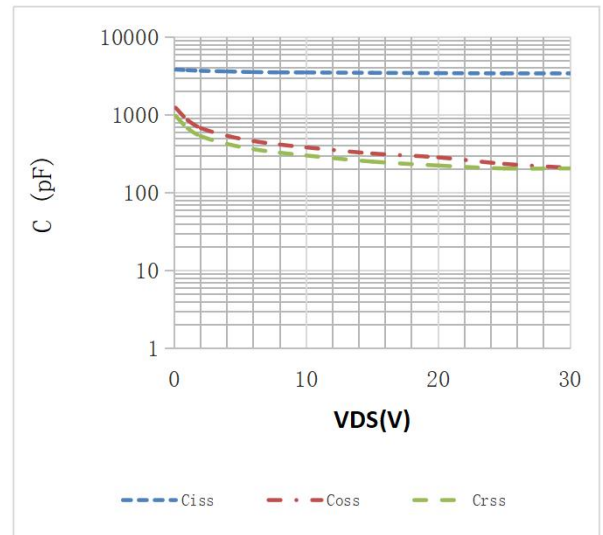


Fig.3 Power Dissipation

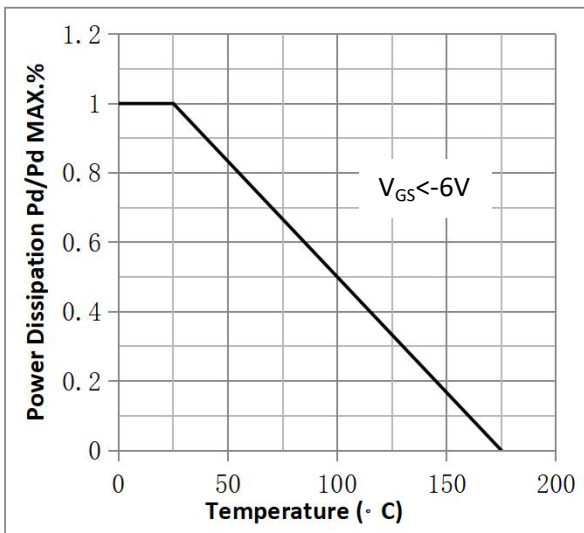


Fig.4 Typical output Characteristics

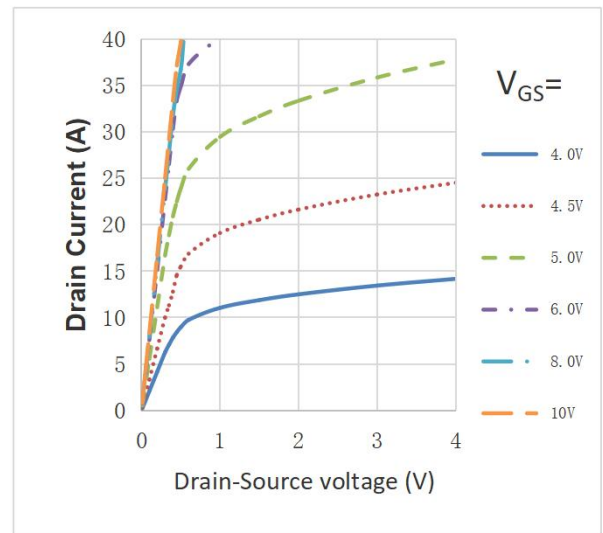


Fig.5 Threshold Voltage V.S Junction Temperature

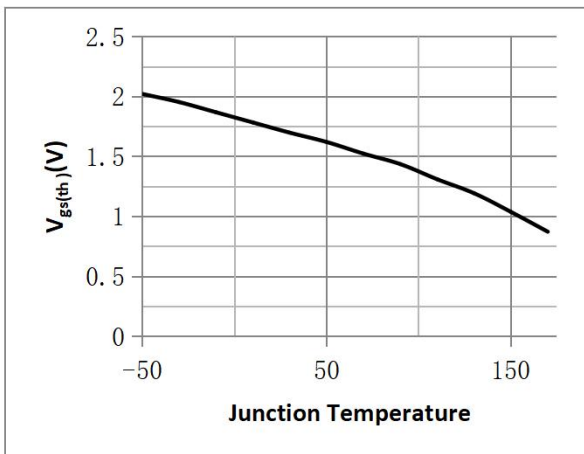


Fig.6 Resistance V.S Drain Current

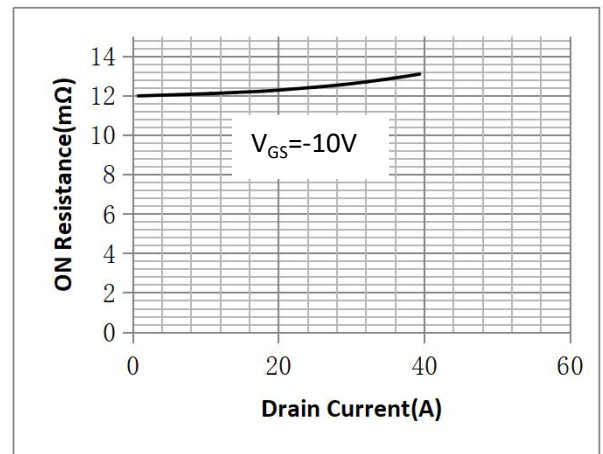


Fig.7 On-Resistance VS Gate Source Voltage

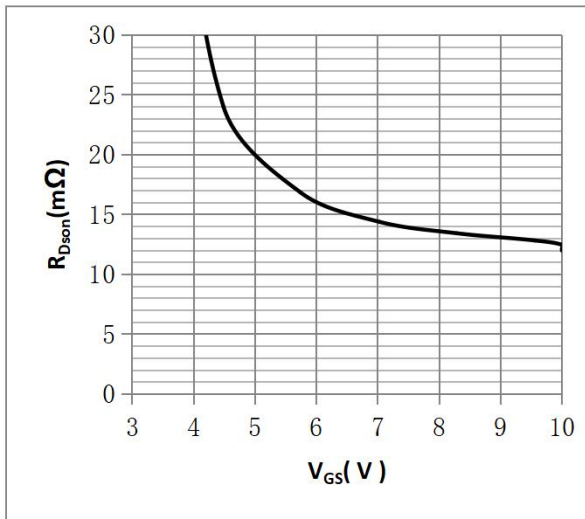


Fig.8 On-Resistance V.S Junction Temperature

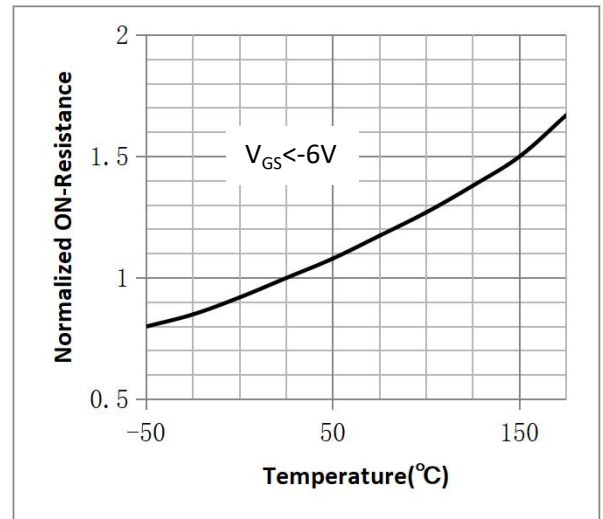


Figure 9. Diode Forward Voltage vs. Current

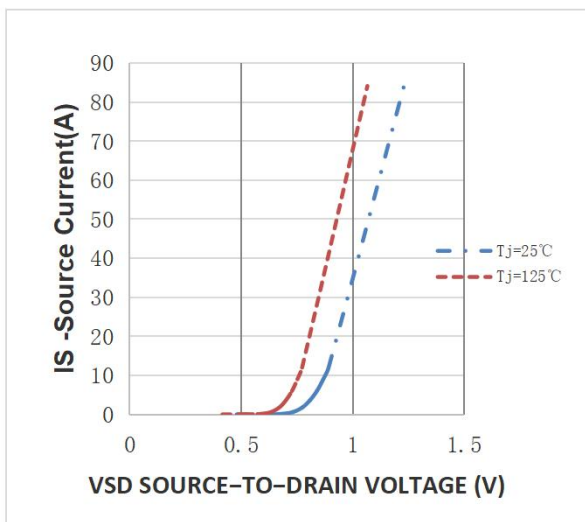


Figure 10. Transfer Characteristics

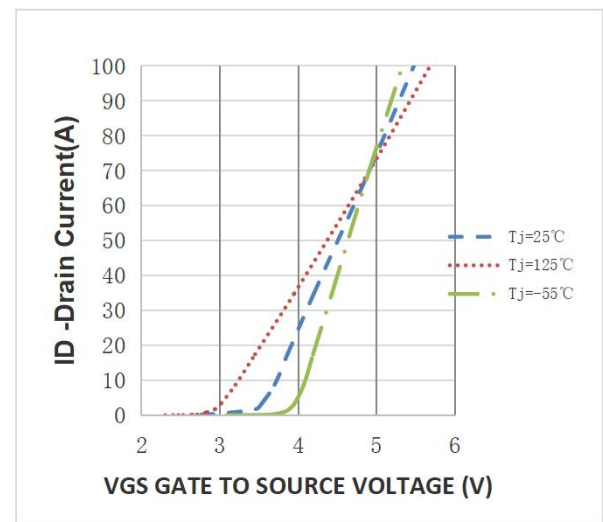


Fig.11 Safe Operating Area

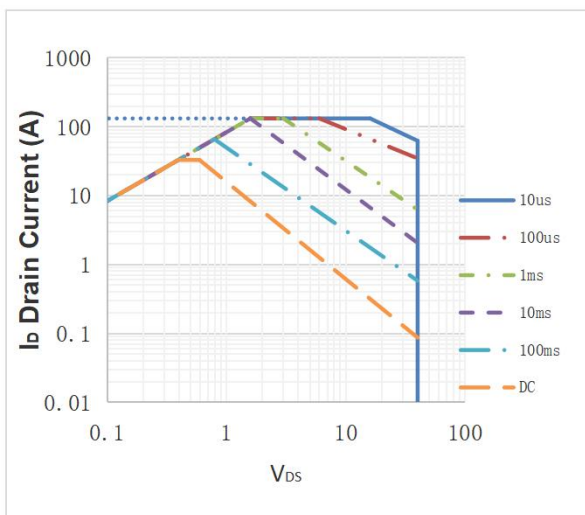
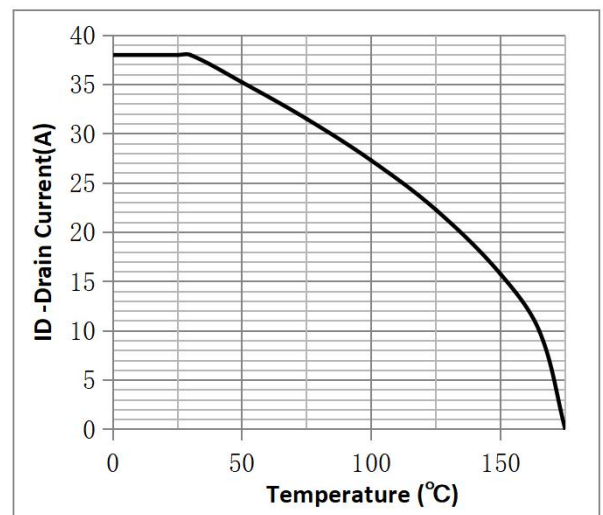
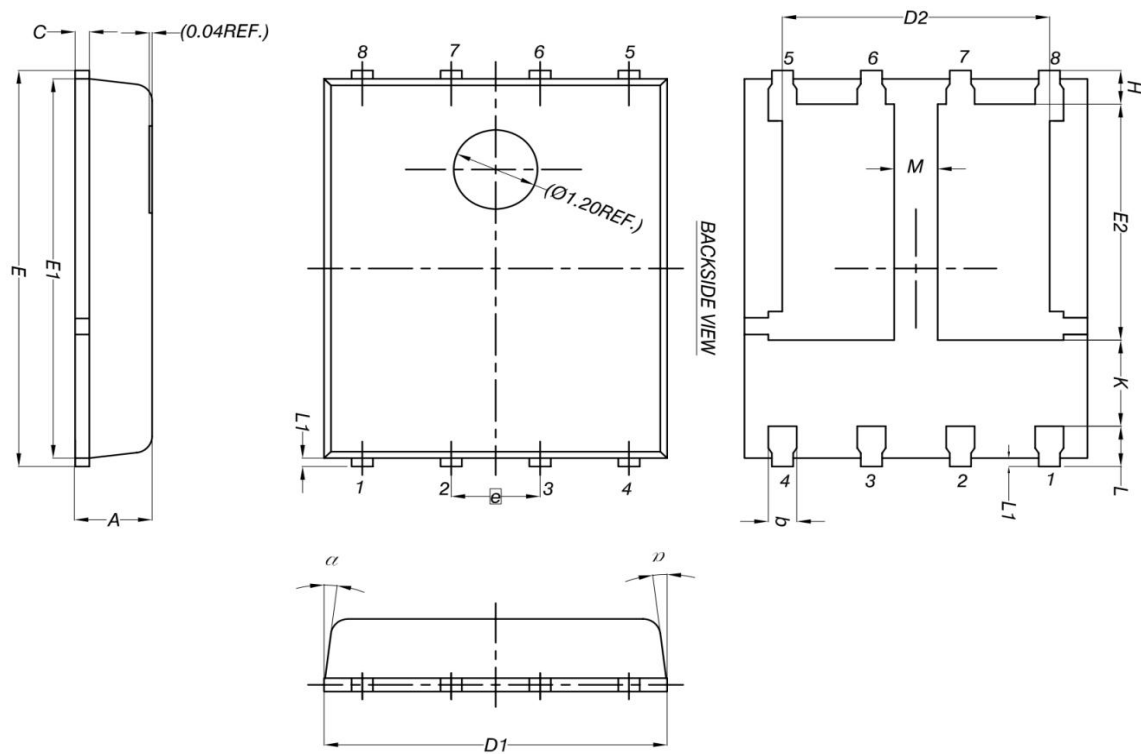


Fig.12 ID vs. Case Temperature^④

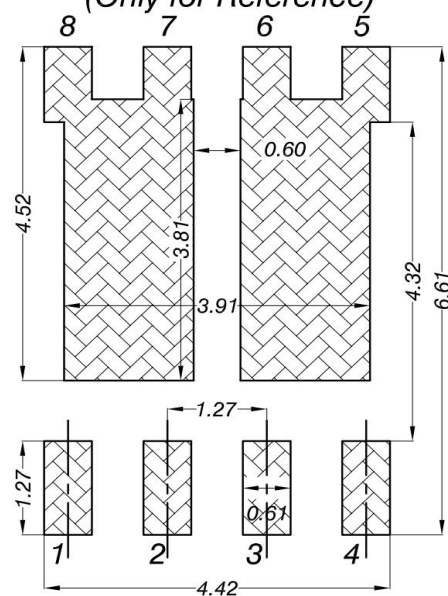


•DFN5*6 Package Outline



DIM.	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
b	0.33	0.41	0.51
C	0.20	0.25	0.30
D1	4.80	4.90	5.00
D2	3.61	3.81	3.96
E	5.90	6.00	6.10
E1	5.70	5.75	5.80
E2	3.38	3.58	3.78
	1.27 BSC		
H	0.41	0.51	0.61
K	1.10	-	-
L	0.51	0.61	0.71
L1	0.06	0.13	0.20
M	0.50	-	-
α	0°	-	12°

Land Pattern
(Only for Reference)



Note:

- ① Pulse : $V_{GS}=+20V/-20V$, Duty cycle=50%, $T_j=175^{\circ}C$, $t=1000$ hours; For DC , the following test conditions can be passed: $V_{GS}=+20V/-10V$, $T_j=175^{\circ}C$, $t=1000$ hours;
- ② Pulse : $V_{GS}=+20V/-20V$, Duty cycle=50%, $T_j=175^{\circ}C$, $t=1000$ hours; For DC , the following test conditions can be passed: $V_{GS}=-20V/+10V$, $T_j=175^{\circ}C$, $t=1000$ hours;
- ③ Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate;
- ④ Practically the current will be limited by PCB, thermal design and operating temperature. $V_{GS}=10V$ (N channel)/-10V(P channel).

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Revision History

Version	Date	Change
A	2021.10.16	NEW
B	2021.11.12	Modifyied the ID curve
C	2022.12.5	Add Dynamic Characteristics , correct the marking
D	2023.11.21	Add Vgs>6v in the curve,correct ID